Application Note: AN00238

SPI Slave Boot Loader

xTIMEcomposer tools support booting over JTAG or from a SPI flash, optionally with encryption and authentication using on-chip OTP. xCORE-200 devices also have a SPI slave boot mode. With a simple custom boot loader, it is possible to boot a two-tile XU216/XL216 device as SPI slave, with another device, such as applications processor, supplying the image to boot, and taking care of any extra features such as firmware upgrade.

This document describes how to use a custom boot loader that was developed for this.

Prerequisites

- This document assumes familiarity with the XMOS xCORE architecture, the XMOS tool chain and the xC language. Documentation related to these aspects which are not specific to this application note are linked to in the references appendix.
- For descriptions of XMOS related terms found in this document please see the XMOS Glossary[1].

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[1] http://www.xmos.com/published/glossary